

3-31-05

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:  
**Gordon Ma et al.**

Serial No.: 10/663,051

Filing Date: September 15, 2003

Title: LDMOS Transistor



Group Art Unit: 2811

Examiner: **James M. Mitchell**

Attny. Docket No. 068736.0232

Client Ref.: FIN 674US hs/eb

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

CERTIFICATE OF MAILING VIA EXPRESS MAIL

PURSUANT TO 37 C.F.R. § 1.10, I HEREBY CERTIFY THAT I HAVE INFORMATION AND A REASONABLE BASIS FOR BELIEF THAT THIS CORRESPONDENCE WILL BE DEPOSITED WITH THE U.S. POSTAL SERVICE AS EXPRESS MAIL POST OFFICE TO ADDRESSEE, ON THE DATE BELOW, AND IS ADDRESSED TO:

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A handwritten signature in cursive script, appearing to read "Tom Shnessay".

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Sir:

**RESPONSE TO FINAL OFFICE ACTION DATED February 17, 2005**

In response to the Final Office Action mailed February 17, 2005, Applicants respectfully request s reconsideration of the rejections set forth in the Office Action in light of the remarks below.

As the two-month shortened statutory period for reply is due April 17, 2005, this Response is therefore considered to be timely filed.